DRAFT NPIC/TDS--8 November 1967

•	MEMORANDUM FOR THE RECORD  SUBJECT: Meeting Concerning the	
:	Funding Situation Chip Comparator	25X1
	ATTENDEES:	25X′
:		
<u>.</u>	1. At 0900 hours on the 8th of November the above individuals	
	pending increased funding request of to discuss the pending increased funding request of for the 405AM and 405B Chip Comparators. The meeting generally preceded along the	25X 25X
	line of the six questions previously circulated thru (copy attached)	25X′
+	2. The financial investment was reviewed with as outlined on the "Chip Comparator Financial Status," (copy attached). In addition	25X′
1	to approving the request funds of a problem exists as to what budget the funds could be appropriated from. The P&M Staff was assigned the responsibility of making this determination. Some discussion evolved	25X <sup>2</sup>
5X1A	funds stated that the 5 504B comparators were numbered from	
5X1A	class 31 funds and that the additional funds should therefore come from class 31 funds. stated that since the situation was somewhat indefensible, he did not want the matter getting out of the building and	
	requested that cost estimates, for providing the necessary building	25X1
	facilities and spare parts, be prepared and furnished him as part of the total financial commitment. Not discussed but an integral part of the Chip Comparator installation cost is the cost of relocating the Film	OEV.
: :	require relocating existing facilities as well as having the manufacturer	25X′
	REVIEW by NIMA/DOD	
:	Approved For Release 2002/66/17: CIA-RDP78B04747A001500030008-5	!

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	3. Concerning the time delay in formally telling 25X1
	of our concern over the performance of the comparators, it was pointed
25X1A	out to that TDS had been attempting to resolve the difficulties
	informally on a working level prior to the letter of 8 July and that
25X1A	personnel were sufficiently aware of our problems with the
	comparators prior to that time, but had made little progress in resolving
	the difficulties. The lapsed time was also the direct result of internal
	difficulties such as, 1) the delays in delivery of peripheral equipment
_	necessary to fully operate the equipment, 2) the restrictions on available
	computer time and check out software, and 3) the lack of qualified test
	and evaluation personnel.
	4. In relation to the question of who in the Center is responsible
	for checking to certify if equipment operates properly, considerable
	discussion evolved. Traditionally the project monitor, on a R&D item,
	is responsible for the acceptance of the equipment and coordinates the
	operation of the equipment with the operational component when and if they
	receive it. It was generally agreed that the procedures for overall
	acceptance of equipment, other than R&D items, and determination of its
	utilization or effectiveness were largely undefined. It was suggested that
	TDS be assigned the responsibility of monitoring the equipment procurement
1 Hori	program and the subsequent utilization of that equipment, however,
T. all M	recommended that function more properly belonged in the P&M Staff because
20 5ML	of their overall planning and budget responsibilities, and 25X1
m jen -	stated that they were working out procedures to control the
25X1A	

Approved For Release 2902/04/12 CIA-RDP78B04747A001500030008-5

shopping list.

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	5. Concerning the operational status of the comparators, the
	prototype, 405AM, has been operational in IAS since the 23rd of August
	without any maintenance adjustments or downtime. Two of the 405B's
25X1A	are now back at being reworked and modified and are tenta-
	tively scheduled for redelivery to the NPIC about the 27th of November.
	The remaining three machines are still at the Center in a disassembled
	form awaiting shipment back to the manufacture. It was also pointed out
	that the prototype 405AM has only been field modified and that to insure
	optimum performance it must also be returned to the factory for permanent
	modifications.
	6. Concerning the Chip Comparators utility with future systems, it was
	explained that the comparators are basic mensuration devices similar to
1A	the existing Comparators and are capable of performing precision
	mensurations on any conventional photographic material within the same
1A	limitations as the Comparators, except for format size (for the status
	of the present system capabilities see the attached memo from Chief, IPD).
	7. It was generally accepted that the rational for purchasing the 405B
	Chip Comparators prior to testing the 405A prototype was a direct resultant
	of the fiscal year problem of allocating uncommitted funds. There was no
	discussion concerning testing procedures on the prototype, or the certification
	of the acceptability of the prototype. The problem of acceptance testing
25X1A	in general was discussed stated that he preferred to have performance
$\cap$	and acceptability tests of complicated instrumentation requiring special
	building facilities or extensive relocating costs, performed in the area in which
	the equipment would eventually be used operationally.

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5X1A	8. raised the questions as to whether we had a requirement	
	for all six Chip Comparators stated that they had not	25X1
	attempted to use the IAS comparator to assist in determination of their	
	requirements but believe that they had a firm requirement for both machines.	
	TID also stated that they had a definate requirement for their machine.	
	9. The meeting broke up with the understanding that there was little	
•	choice but to approve the expenditure of funds as requested and that the	
	P&M Staff had the responsibility to take the necessary action.	
5X <b>2</b> 4	10. After the meeting, briefed on the planning	25X1
	and development of other NPIC sponsored chip handling equipment, showing	•
	the realtionship of the Chip Comparators with the other equipment.	

Technical Development Staff

Attachments Distribution

1 copy each of the draft to the attendees for comments

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